

WEST Search History

DATE: Monday, January 24, 2005

| Hide? | <u>Set</u> <u>Name</u> | <u>Query</u> | <u>Hit</u> <u>Count</u> |
|--------------------------|--|---|----------------------------|
| | <i>DB=USPT,PGPB; PLUR=NO; OP=ADJ</i> | | |
| <input type="checkbox"/> | L82 | L81 and (L40 or L51 or L46) | 18 |
| <input type="checkbox"/> | L81 | ((712/229)!.CCLS.) | 269 |
| <input type="checkbox"/> | L80 | L79 and (L40 or L51 or L46) | 14 |
| <input type="checkbox"/> | L79 | ((712/226)!.CCLS.) | 331 |
| <input type="checkbox"/> | L78 | L74 and condition code\$1 | 52 |
| <input type="checkbox"/> | L77 | L74 and L46 | 2 |
| <input type="checkbox"/> | L76 | L74 and L51 | 16 |
| <input type="checkbox"/> | L75 | L74 and L40 | 4 |
| <input type="checkbox"/> | L74 | ((712/227)!.CCLS.) | 516 |
| <input type="checkbox"/> | L73 | L69 same instruction\$1 | 102 |
| <input type="checkbox"/> | L72 | L69 same (emulat\$3 or native or risc) | 8 |
| <input type="checkbox"/> | L71 | L69 with (emulat\$3 or native or risc) | 1 |
| <input type="checkbox"/> | L70 | L69 same emulat\$3 | 4 |
| <input type="checkbox"/> | L69 | enabl\$3 with (modification or alteration or chang\$3 or modify\$3 or alter\$4) with (flag\$1 or (condition code\$1)) | 748 |
| <input type="checkbox"/> | L68 | L63 and condition code\$1 | 35 |
| <input type="checkbox"/> | L67 | L66 and condition code\$1 | 0 |
| <input type="checkbox"/> | L66 | L63 and L46 | 0 |
| <input type="checkbox"/> | L65 | L63 and L51 | 12 |
| <input type="checkbox"/> | L64 | L63 and L40 | 2 |
| <input type="checkbox"/> | L63 | ((703/26)!.CCLS.) | 300 |
| <input type="checkbox"/> | L62 | 5630137.uref. | 3 |
| <input type="checkbox"/> | L61 | 6000028.uref. | 9 |
| <input type="checkbox"/> | L60 | 5768574.uref. | 5 |
| | <i>DB=JPAB,EPAB,DWPI,TDBD; PLUR=NO; OP=ADJ</i> | | |
| <input type="checkbox"/> | L59 | L58 with (emulat\$3 or native or risc) | 5 |
| <input type="checkbox"/> | L58 | (condition code\$1 or flag\$1 or status register) with (modif\$7 or destruction or destroy\$3 or overwrit\$3 or over-writ\$3) | 504 |
| <input type="checkbox"/> | L57 | condition code modification | 1 |
| <input type="checkbox"/> | L56 | flag modification | 6 |
| | <i>DB=USPT,PGPB; PLUR=NO; OP=ADJ</i> | | |

| | | | |
|--------------------------|-----|---|--------|
| <input type="checkbox"/> | L55 | L53 with (emulat\$3 or native or risc) | 0 |
| <input type="checkbox"/> | L54 | L51 with condition code\$1 | 2 |
| <input type="checkbox"/> | L53 | L51 with flag\$1 | 199 |
| <input type="checkbox"/> | L52 | L51 with status register | 12 |
| <input type="checkbox"/> | L51 | prevent\$3 with (modif\$7 or destruction or destroy\$3 or overwrit\$3 or over-writ\$3) | 55765 |
| <input type="checkbox"/> | L50 | L49 not L48 | 2 |
| <input type="checkbox"/> | L49 | L46 with (modif\$7 or destruction or destroy\$3 or overwrit\$3 or over-writ\$3) | 51 |
| <input type="checkbox"/> | L48 | L46 with (modif\$7 or (destruction or destroy\$3 or overwrit\$3 or over-writ\$3) over-writ\$3) | 49 |
| <input type="checkbox"/> | L47 | L46 with emulat\$3 | 0 |
| <input type="checkbox"/> | L46 | protect\$3 with status register | 255 |
| <input type="checkbox"/> | L45 | L40 with instruction\$1 | 67 |
| <input type="checkbox"/> | L44 | L40 with risc | 0 |
| <input type="checkbox"/> | L43 | L40 with native | 4 |
| <input type="checkbox"/> | L42 | L40 with emulat\$3 | 5 |
| <input type="checkbox"/> | L41 | L40 with (destruction or destroy\$3 or overwrit\$3 or over-writ\$3) | 70 |
| <input type="checkbox"/> | L40 | protect\$3 with (flag\$1) | 1889 |
| <input type="checkbox"/> | L39 | protect\$3 with (condition code\$1) | 44 |
| <input type="checkbox"/> | L38 | L32 not L35 | 68 |
| <input type="checkbox"/> | L37 | L32 not L34 | 86 |
| <input type="checkbox"/> | L36 | L1L4 | 9 |
| <input type="checkbox"/> | L35 | L32 and emulat\$3 | 18 |
| <input type="checkbox"/> | L34 | (risc and cisc) and L32 | 0 |
| <input type="checkbox"/> | L33 | native and L32 | 1 |
| <input type="checkbox"/> | L32 | (over-writ\$3 or overwrit\$3) with flag\$1 with prevent\$3 | 86 |
| <input type="checkbox"/> | L31 | cisc with risc with emulat\$4 | 86 |
| <input type="checkbox"/> | L30 | L25 and L29 | 24 |
| <input type="checkbox"/> | L29 | L26 with (modification or alteration or chang\$3 or modif\$3 or alter\$3) | 21226 |
| <input type="checkbox"/> | L28 | L25 and L26 | 106 |
| <input type="checkbox"/> | L27 | L24 and L26 | 8 |
| <input type="checkbox"/> | L26 | (condition code\$1 or status code\$1 or status bit\$1 or flag\$1) | 138689 |
| <input type="checkbox"/> | L25 | (5805895 or 3955180 or 5970242 or 5619665 or 5826089 or 5925123 or 5875336 or 5937193 or 5953520 or 6021469 or 5568646 or 5758115 or 5367685).uref. | 233 |
| <input type="checkbox"/> | L24 | (5805895 or 3955180 or 5970242 or 5619665 or 5826089 or 5925123 or 5875336 or 5937193 or 5953520 or 6021469 or 5568646 or 5758115 or 5367685).pn. | 13 |
| | | <i>DB=TDBD; PLUR=NO; OP=ADJ</i> | |
| <input type="checkbox"/> | L23 | (5805895 or 3955180 or 5970242 or 5619665 or 5826089 or 5925123 or 5875336 or 5937193 or 5953520 or 6021469 or 5568646 or 5758115 or 5367685).pn. | 0 |
| <input type="checkbox"/> | L22 | (real-time cisc).ti. | 1 |
| | L21 | (op code and status).ti. | 1 |

| | | | |
|--------------------------|--------------------------------------|--|------|
| <input type="checkbox"/> | | | |
| | <i>DB=USPT,PGPB; PLUR=NO; OP=ADJ</i> | | |
| <input type="checkbox"/> | L20 | 5590359.uref. | 5 |
| <input type="checkbox"/> | L19 | L16 and (condition code\$1).ti,ab,clm. | 11 |
| <input type="checkbox"/> | L18 | L12 and L17 | 0 |
| <input type="checkbox"/> | L17 | L7 and L16 | 0 |
| <input type="checkbox"/> | L16 | ((712/228)!.CCLS.) | 493 |
| <input type="checkbox"/> | L15 | L12 not L14 | 12 |
| <input type="checkbox"/> | L14 | L12 not L13 | 125 |
| <input type="checkbox"/> | L13 | L12 and risc | 12 |
| <input type="checkbox"/> | L12 | (L5 or L6) with sav\$3 | 137 |
| <input type="checkbox"/> | L11 | L7 not L10 | 42 |
| <input type="checkbox"/> | L10 | L7 not L8 | 831 |
| <input type="checkbox"/> | L9 | L7 not L8L8 | 873 |
| <input type="checkbox"/> | L8 | L7 and risc | 42 |
| <input type="checkbox"/> | L7 | (L5 or L6) with updat\$3 | 873 |
| <input type="checkbox"/> | L6 | call with (status) | 9608 |
| <input type="checkbox"/> | L5 | call with (condition code\$1) | 100 |
| <input type="checkbox"/> | L4 | L3 and ((condition code\$1) with updat\$3) | 3 |
| | | (4181942 4270181 4498135 4503500 4566063 4742453 4881194 4891754 4935867 | |
| <input type="checkbox"/> | L3 | 4964046 4974155 5045993 5051896 5053954 5081574 5109495 5185869 5193157 | 21 |
| | | 5257354 5313644 5345567)! [pn] | |
| | <i>DB=USPT; PLUR=NO; OP=ADJ</i> | | |
| <input type="checkbox"/> | L2 | kiuchi.in. and (digital signal processor).ti. | 3 |
| | <i>DB=EPAB,DWPI; PLUR=NO; OP=ADJ</i> | | |
| <input type="checkbox"/> | L1 | kiuchi.in. and (digital signal processor).ti. | 5 |

END OF SEARCH HISTORY